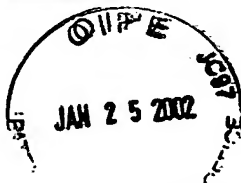


Docket No.: WMP-IFT618



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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231, on the date indicated below.

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Date: 1/4/02 ^{02 MPH}

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Ralf Otremba et al.
Applic. No. : 10/008,241
Filed : November 8, 2001
Title : Electronic Component with Isolation Barriers Between the Terminal Pins

INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

United States Patent No. 3,095,526 (Thornton), dated June 25, 1963;

German Utility Model G 69 48 923 dated December 18, 1969, semiconductor rectifier assembly for high peak currents;

German Examination Report dated July 19, 2001.

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications,

patents and/or publications, it is because no existing translation is readily available to the applicant.

Respectfully submitted,



For Applicants

Mark P. Weichselbaum
Reg. No. 43,248

Date: January 4, 2002

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